

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of Invention	[LOW POWER CONSUMPTION CIRCUIT AND DELAY CIRCUIT THEREOF]
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Application Number :

Confirmation Number:

First Named Applicant: Hong-Gee Fang

Attorney Docket Number: 11425-US-PA

Art Unit:

Examiner:

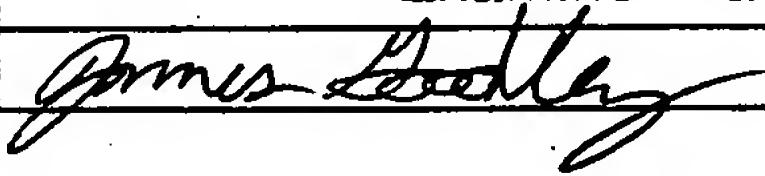
Search string: (6188293).pn

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
JG	1	6188293	2001-02-13	Miyagi , et al.		331	57

Signature

Examiner Name	Date
	10/25/2005